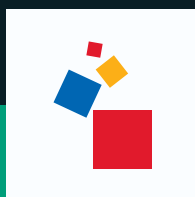


pcim

ASIA NEW DELHI conference

9 – 10 DECEMBER 2025
NEW DELHI, INDIA



mesago

The Agent of Change for the Indian Power Electronics Industry

CONFERENCE BOOKLET

SUPPORTED BY



इलेक्ट्रॉनिक्स एवं
सूचना प्रौद्योगिकी मंत्रालय
MINISTRY OF
**ELECTRONICS AND
INFORMATION TECHNOLOGY**

सत्यमेव जयते



विद्युत मंत्रालय
MINISTRY OF
POWER

सत्यमेव जयते

MESSAGE FROM

MR. S. KRISHNAN, IAS, SECRETARY,
MINISTRY OF ELECTRONICS & IT, GOVT. OF INDIA

एस. कृष्णन, आई.ए.एस.
सचिव
S. Krishnan, I.A.S.
Secretary



इलेक्ट्रॉनिक्स और सूचना प्रौद्योगिकी मंत्रालय
भारत सरकार
Ministry of Electronics &
Information Technology (MeitY)
Government of India

MESSAGE

I am pleased to learn that Messe Frankfurt Group is launching the PCIM Asia New Delhi Conference scheduled for 9-10 December 2025 at Dr. Ambedkar International Centre, Delhi to serve as a vital platform for industry and academia experts to showcase and discuss the latest cutting-edge research and innovations in the Power Electronics and its Applications.

Power electronics plays a crucial role in driving electrification and energy efficiency, serving as the backbone of India's rapidly evolving infrastructure across renewable power, power grids, electric mobility, railways, and data centers. I am confident that the PCIM Asia New Delhi Conference will further accelerate advancements in this domain, strengthening the broader electronics ecosystem.

I appreciate Messe Frankfurt India for bringing the brightest innovators and entrepreneurs on common platform to share their ideas, innovations and solutions in power electronics and its applications.

I extend my best wishes for the success of the event and look forward to its positive impact on all stakeholders.


(S. Krishnan)

Place: New Delhi
Date: 25th April, 2025



Indian Power Electronics Industry – An Overview

Forging a Global Hub for Innovation and Manufacturing

The Indian Power Electronics industry is at the cusp of phenomenal growth. While on one hand it is propelled by a resolute policy support and conducive investment environment created by the Ministry of Electronics and Information Technology (MeitY), to boost domestic R&D and manufacturing of high-end power electronics, it is backed by a surge in demand from sustained and significant infrastructure expansion in the major application areas including the following:

1 Comprehensive emphasis on energy efficiency and energy transition via electrification.

2 Scaling up EV manufacturing & deploying a nationwide EV charging infrastructure.

3 Constant enhancement in India's installed renewable energy capacity (solar & wind power generation, green hydrogen production).

4 Upgradation of the power grid and power transmission & distribution network; renewable power integration.



5 Boost in domestic manufacturing of mobile phones, and other consumer electronics; advent of 6G technology.

6 Ramp-up in the manufacturing of electric locomotives and trainsets; expansion of electrified rail network.

7 Multiple new railway projects including metro railways, high speed rail, dedicated freight corridors, regional rail projects.

8 Expansion of hyperscale data centres with AI capabilities.



Conference Tuesday, 9 December 2025

Sessions

8:30 – 9:30

Delegate check-in, onsite registration/collection of Delegate kits; Morning tea/coffee and refreshments

PLENARY INAUGURAL SESSION (Venue: Bhim Hall)

9:30 – 9:45

Ceremonial Lamp Lighting; Inauguration of the Conference and Exhibition

Welcome Note by Organizer

9:45 – 9:50



Mr. Raj Manek, Executive Director & Board Member, Messe Frankfurt Asia Holdings Ltd.

Opening Address

9:50 – 10:00



Prof. (Dr.) Leo Lorenz, President, European Centre for Power Electronics & Member, Steering Committee, PCIM Asia New Delhi Conference

Keynote: Advanced power semiconductor technologies and challenges for pursuing carbon neutrality and sustainability

10:00 – 10:15



Dr. Gourab Majumdar, IEEE Fellow, Entered IEEE-ISPST Inaugural Hall of Fame; Senior Fellow, Mitsubishi Electric Corporation

Keynote by Guest of Honor: Indian Power Electronics Industry – Vision and Immense Opportunities

10:15 – 10:30



Smt. Sunita Verma, Group Coordinator, R&D; Joint Secretary, Ministry of Electronics & IT Govt. of India

Inaugural Keynote by Chief Guest: Transformation and upgradation of Indian Power T&D Sector and Smart Grids – Opportunity for Power Electronics industry

10:30 – 10:45



Dr. D. Sai Baba, Joint Secretary (Transmission & Coordination), Ministry of Power, Govt. of India

10:45 – 10:50

Vote of thanks and felicitation



10:50 – 11:30

Networking tea/coffee and refreshments; Visit to Exhibition

11:30 – 17:30

Conference program continues in two parallel tracks (Detailed program in following pages)



Conference

Tuesday, 9 December 2025

Sessions

Venue: Bhim Hall		Venue: Nalanda Hall	
Application Area Focus Session 1		Technical Session 1	
Power Transmission and Distribution; Power Grid		Packaging And Reliability; Lifetime Prediction; Thermal Management	
 11:30 - 11:50 The Age of Electricity, how Power Electronics will shape the Grid of the Future Dr. Baghaie Yazdi Mehrdad, Vice President- Systems, Applications and Software, Infineon Technologies		 Session Chair: Dr. Vivek Dutta, Advisor, Namics Corporation; Senior Expert & Consultant, Material Science	
 11:50 - 12:40 Transformation of Indian Power T&D infrastructure– Role and opportunities for Power Electronics Industry Panelists: Mr. Abhishek Ranjan, Chief Executive Officer, BSES Rajdhani Power Ltd.  Mr. Hitesh Bhardwaj, Business Head Semiconductors & Devices Mitsubishi Electric India  Mr. Ashok Kumar Rajput, Chairman, Electro - Technical Division Council, Bureau of Indian Standards; Former Member (Power Systems), Central Electricity Authority, Ministry of Power  Mr. Puneet Tyagi, Chief General Manager- Engineering HVDC Power Grid Corporation of India Ltd. Discussion Moderator:  Mr. Anish Mandal, Partner - New Energy and Energy Transition, Deloitte 12:40 - 12:50 Q & A		 11:30 - 11:50 Empowering Performance and Reliability in Power Electronics Manufacturing- Advanced Vacuum Soldering and Sintering Solutions for High-Performance Applications Mr. Thomas Krebs, Technology Consultant, PINK GmbH	
 11:50 - 12:10 Sintering Innovations for Power Electronics Packaging and Assembly Mr. Gyan Dutt, Director, Strategic Marketing, MacDermid Alpha Electronics Solutions		 12:10 - 12:30 Thermal Fatigue-Based Lifetime Prediction Method for Power Semiconductor Devices Mr. Takuma Sakai, Power Semiconductor Application Engineer, Fuji Electric	
 12:30 - 12:50 3.3 kV 2 in 1 High Current Capacity Si-IGBT and SiC-MOSFET Power Modules with Sintered Copper Die Attach Technology Mr. Kan Yasui, Design Development Division, Minebea Power Semiconductor Device Inc.		 12:50 - 13:10 Estimation of power semiconductor junction temperature from package body temperature Mr. Dinesh Palaniappan, System Application Engineer, Infineon Technologies Asia Pacific Pte. Ltd.	

Conference

Tuesday, 9 December 2025

Sessions

Venue: Bhim Hall		Venue: Nalanda Hall	
Application Area Focus Session 2			
Renewable Energy; Renewable Power – Solar, Wind, Green Hydrogen			
	12:50 - 13:05 Power semiconductor technology driving best-in-class power-density, efficiency and cost for solar inverter Dr. Ma Kwok Wai, Senior Principal Engineer, Infineon Technologies		13:10 - 13:30 Thermal Transient Measurement Methods for Cascode Power: Devices to Determine Junction to Package Thermal Resistance Dr. Wasanthamala Badalawa, Pre Sales Solution Consultant, Siemens Digital Industries Software – Digital Factory Division
		13:30 - 13:50 Q & A	
13:05 - 13:45		Panel Discussion	
Building Indigenous Capability in Power Electronics Development for Renewable Energy Growth			
Panelists:			
	Mr. Pujan Doshi, Managing Director, Waaree Renewable Technologies		
	Mr. Atul Abhyankar, Practice Head – Power Electronics, L&T Technology Services		
	Dr. Ma Kwok Wai, Senior Principal Engineer, Infineon Technologies		
	Mr. Xavier Scaria, Vice President, Fimer India Pvt. Ltd.		
Discussion Moderator:			
	Prof. (Dr.) Kaushik Basu, Associate Professor, Indian Institute of Science, Bengaluru		
13:45 - 13:50			
Q & A			
13:50 – 15:00		Networking Lunch; Visit to Exhibition & Poster Presentation zone	

Conference

Tuesday, 9 December 2025

Sessions

Venue: Bhim Hall	Venue: Nalanda Hall
Application Area Focus Session 3 Indian Railways: Traction, Locomotives, Trainsets  Session Chair: Prof. (Dr.) Mukesh Kumar Pathak, Professor, Department of Electrical Engineering, IIT Roorkee  Co-Chair: Prof. (Dr.) Jeevanand Seshadrinath, Associate Professor, Department of Electrical Engineering, IIT Roorkee	Technical Session 2 High-Frequency Power Electronic Converters and Inverters  Session Chair: Mr. Vijay Bolloju, Director- R&D, iVP Semiconductor
 15:00 - 15:20 Keynote Electrification and Energy Efficiency mandate of Indian Railways: Critical role of Power Electronics Dr. Jaideep Gupta, Visiting Professor (Energy Sciences), National Institute of Technology; Former Additional Member (Railway Electrification), Railway Board, Ministry of Railways, Govt. of India  15:20 - 15:40 Building the Trains of Tomorrow: The Power Electronics Opportunity Mr. Debi Prasad Dash, Senior Expert & Industry Consultant - Railways Rolling Stock & System Integration; Former General Manager, Chittaranjan Locomotive Works  15:40 - 16:00 Latest developments in locomotive propulsion technology for Indian Railways Mr. Renji V. Chacko, Senior Director, Head - Power Electronics Group, C-DAC 16:00 - 16:15 Q & A	 15:00 - 15:20 Opening Address by Session Partner High-Fidelity Power Electronics Model Simulation for Sustainable Mobility and Renewable Energy with dSPACE FPGA Platforms Mr. Pavan Hiremath Ashokbabu, Manager E- Mobility, dSPACE GmbH  15:20 - 15:40 Layout Design of 1.2 kV, 11 kW, 100 kHz Matrix Bridge Leg for Single-Stage Three-phase AC to DC Dual Active Bridge based converter Mr. Naresh Rana, Ph.D. Scholar, Indian Institute of Science, Bengaluru  15:40 - 16:00 Mitigating Conducted EMI with SiC Solutions in Renewable Energy & Grid-Connected Power Converters Mr. Sidharth Gupta, Power Applications Engineer, Wolfspeed  16:00 - 16:20 PWM Strategies for VSI's: Analysis, Simulation with Python, Rapid Verification, and Practical Deployment on Modern Microcontrollers Mr. Nabarun Dasgupta, Senior Group Manger, STMicroelectronics 16:20 - 16:35 Q & A

Conference

Tuesday, 9 December 2025

Sessions

Venue: Bhim Hall	Venue: Nalanda Hall
Partner Presentations 16:15 - 16:35 ROHM's Development Strategy for India  Mr. Toshio Nakajima, Group General Manager- Silicon Power Device Business Unit, Rohm Co. Ltd.  Ms. Mona Patil, Assistant Manager- Silicon Power Device Business Unit, Rohm Semiconductor India 16:35 - 16:55 Power Conversion for Electrification - Enabled by Power Electronics Packaging & Assembly Dr. Ravi M. Bhatkal, Managing Director – India, MacDermid Alpha Electronics Solutions  16:55 - 17:15 SEMITRANS 20 - Setting Standards in SiC for Renewable Applications Mr. Erwin Ysewijn, Regional Segment Marketing, Semikron Danfoss 17:15 - 17:30 Q & A	Application Area Focus Session 4 Expansion of Global Data Centre Infrastructure in India – Emergence as a major Power demand segment  Session Chair: Mr. Atul Abhyankar, Practice Head – Power Electronics L&T Technology Services  16:35 - 16:55 Evolutionary Trends in Power Supply for AI Datacenters Mr. Frank Heerdt, Power Electronics Expert, Huawei Ohm Research Center, Germany  16:55 - 17:15 Taming the Energy Curve: Managing AI and Data Processing Power Needs Mr. Preetam Tadeparthy, Chief Technology Officer & Vice President – Engineering, c2i Semiconductors 17:15 - 17:30 Q & A
17:30 – 18:15 Networking tea/coffee & refreshments; Visit to Exhibition & Poster Presentation zone	



Conference

Wednesday, 10 December 2025

Sessions

9:00 – 10:00	Morning tea/coffee and refreshments
Venue: Bhim Hall	Venue: Nalanda Hall

Partner Presentations

Technical Session 3

Automotive Power Electronics And Electric Mobility



Session Chair:
Prof. (Dr.) Sandeep Anand, Professor, Department of Electrical Engineering & In-charge, C1973 EV Powertrain Lab, IIT Bombay



10:00 - 10:20

Bosch SiC Technology - Ensuring Efficiency, Robustness, and Long-Term Safety

Mr. Steffen Beushausen, Technical Expert Power Electronics, Bosch (China) Investment Ltd.



10:20 - 10:40

High Thermal and High Reliability Die Attach for Power Electronics Applications

Mr. Ram K. Trichur, Global Head - Semiconductor Packaging, Henkel



10:40 - 11:00

Powering India's Electrification: High-Efficiency RC-IGBT Solutions

Mr. Raveendar Sekar, Assistant General Manager - Semiconductor Division, Fuji Electric India Pvt Ltd.

11:00 - 11:10

Q & A



10:00 - 10:20

Cutting-edge Developments in Power Electronics for EV Charging Infrastructure

Mr. Frank Heerdt, Power Electronics Expert, Huawei Ohm Research Center, Germany



10:20 - 10:40

Optimizing MOSFET Selection for the 48 V Hybrid Mobility Market

Mr. Sacha Cazzitti, Ph.D. Scholar, The University of Manchester, UK



10:40 - 11:00

EZ-PD™ PAG2S: 48 V input voltage Synchronous Buck with integrated USBPD EPR

Ms. Manvi Gupta, Staff Applications Engineer Infineon Technologies Pvt. Ltd.

11:00 - 11:10

Q & A

11:10 – 11:40	Networking tea/coffee & refreshments; Visit to Exhibition
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Scan QR to Read Speaker Bios



Conference

Wednesday, 10 December 2025

Sessions

Venue: Bhim Hall	Venue: Nalanda Hall
Partner Presentations  11:40 - 12:00 Mitsubishi Electric contributions to key Power Electronics application areas in India Mr. Hitesh Bhardwaj, Business Head Semiconductors & Devices Mitsubishi Electric India  12:00 - 12:20 A Partnership for India's Future: How Japan's Core Technology will Power India's National Growth Mr. Masahiko Suzuki, President, Minebea Power Semiconductor Devices Inc. 12:20 - 12:35 Q & A Technical Session 4 Smart Grid Power Electronics: PV Energy Systems and Novel Converter Topologies  Session Chair: Prof. (Dr.) Bhim Singh, Professor Emeritus, Dept. of Electrical Engineering, IIT Delhi  12:35 - 12:55 Powering a Sustainable Future – TMEIC's Journey in Clean Technology Innovation Mr. K. B. Sreekumar, Chief Operating Officer, TMEIC Industrial Systems India Pvt. Ltd.  12:55 - 13:15 High Power Density Multi-Phase DC/DC Converter for Solar Applications Using GaN Integrated Power Stages Mr. Parvraj Pachore, Staff Engineer, Infineon Technologies  13:15 - 13:35 Development of a Novel Single-Source Three-Phase Step-Up Multilevel Inverter Topology Mr. Yadvendra Singh, Ph.D. Scholar, Thapar Institute of Engineering & Technology 13:35 Q & A	Technical Session 3 (Contd.) Automotive Power Electronics and Electric Mobility  Session Chair: Dr. Venkatesh Raghavan, VP, PFC (Energy Solutions Business Division) TDK India Pvt. Ltd.  11:40 - 12:00 Application of Nanocrystalline Soft Magnetic materials in E-Mobility for high-power density power electronics Mr. Bharadwaj Reddy Andapally, Technical Market Development, CBMM  12:00 - 12:20 A Versatile Two-Port Extreme Fast Charger with Wide EV Range Application Mr. Shivrajit Som, Ph.D. Scholar, Indian Institute of Technology, Delhi  12:20 - 12:40 Open Switch fault diagnosis in T-type multilevel inverter using machine learning for EV applications Dr. Niraj Kumar Dewangan, Assistant Professor, Manipal Institute of Technology, Manipal Academy of Higher Education  12:40 - 13:00 Data-driven generation and validation of synthetic EV drive cycles using GMM and HSMM techniques Dr. Manas Ranjan Sial, Research Engineer, Automotive Research Association of India 13:00 - 13:15 Q & A 13:15 - 13:35 Future of Sovereign AI and custom Silicon in India  Mr. Jim Keller, Chief Executive Officer, Tenstorrent  Mr. Bapi Dash, Co-Founder, QBlocks

13:35 – 15:00

Networking Lunch; Visit to Exhibition & Poster Presentation zone

Venue: Bhim Hall

Venue: Nalanda Hall

Technical Session 5

Advanced Power Semiconductors (High Power Semiconductors; SiC Devices)



Session Chair:
Dr. Ritu Sodhi, Technical Consultant - Power Semiconductors & Advisor, SemiX - Center of Excellence in Semiconductors, IIT Bombay



15:00 - 15:20
ASFET-Based Approach for Enhanced Dynamic Current Sharing in Paralleled Power Devices
Mr. Aniket Kulkarni, Sr. Power Applications Engineer, Nexperia



15:20 - 15:40
>3 kV 4H-SiC MOSFETs with Design and Performance Evaluation
Dr. Monikuntala Bhattacharya, SiC Engineer, L&T Semiconductor Technologies



15:40 - 16:00
Comprehensive Characterization of SiC Schottky Barrier Diodes From Room Temperature Down to 7K
Ms. Sanchari Guha, Doctoral Student, Kyoto Institute of Technology, Japan



16:00 - 16:20
Single-Pulse Short Circuit Ruggedness of Different 650V SiC MOSFET Cell Designs
Mr. Anshul Tyagi, Sr. Product Definition Engineer, Infineon Technologies



16:20 - 16:40
New 4.5 kV HV - IGBT Module for High-speed Switching
Mr. Yuta Fukushima, High Voltage Device Design & Development Sect. Power Semiconductor Device Dept.b Mitsubishi Electric Corporation Power Device Works

16:40 - 17:00
Q & A

Valedictory Session

17:00 - 17:20

Announcement and Presentation of Awards in Categories

- Best Technical Paper
- Best Technical Poster
- Young Researcher



17:20 - 17:30

Valedictory Address and Vote of Thanks by Organizer

14:30 - 16:00

Tutorial Session 1



Session Chair:
Dr. Gourab Majumdar, IEEE Fellow, Entered IEEE-ISPSP Inaugural Hall of Fame; Senior Fellow, Mitsubishi Electric Corporation



Advance Si+SiC chip technology & Chip Biz
Dr. Koichiro Noguchi, General Manager Product Strategy Dept. Mitsubishi Electric Corporation Power Device Works



STACK design example for MW class renewable energy
Dr. Zhengfeng Li, Manager Marketing and Application Engineering Sect.B Product strategy Dept., Mitsubishi Electric Corporation Power Device Works



DIPIM case study for small power inverters
Mr. Pradeep Bisht, Deputy General Manager Application Engineering Semiconductors & Devices, Mitsubishi Electric India



Our approach to the Automotive application area
Mr. Pradeep Bisht, Deputy General Manager Application Engineering Semiconductors & Devices, Mitsubishi Electric India

16:00 - 17:30

Tutorial Session 2



Session Chair:
Mr. Saumitra Singh, Director, Infineon Technologies



Overcoming SiC MOS application challenges body diode, protection, layout and paralleling
Dr. Ma Kwok Wai, Senior Principal Engineer, Infineon Technologies

17:30 – 18:15

Networking tea/coffee & refreshments; Visit to Exhibition & Poster Presentation zone

Conference

9 - 10 December 2025

13:30 – 15:00 | 17:30 – 18:15

Networking and Lunch area on the 1st floor

Poster Presentations

Power Electronics In Transmission Systems And Smart Grid Applications



Chair:
Prof. (Dr.) Jeevanand Seshadrinath,
Associate Professor, Department of
Electrical Engineering, IIT Roorkee

Academia



Advanced Analysis of Voltage Rise Phenomena in Electrically Floating HV Power Platforms.
Mr. Kapila Warnakulasuriya, Honorary
Professor Teesside University, UK

Academia



Application of Artificial Neural Network for High Impedance Fault Detection in Power System
Ms. Vaishnavi Petkar, Student, M.Tech,
Walchand College of Engineering, Sangli

Industry



Integrated LC filter design for High Power Charging Systems using 12 Pulse Thyristor Rectifiers
Mr. Karthik Ankam, Research & Development
Engineer, Hitachi Energy India

Academia



Hardware Design of Cascode GaN Module for Education and Research
Ms. Little Pradhan, Ph.D. Scholar Indian
Institute of Technology, Dharwad

Industry



A Hybrid Tsukamoto Fuzzy-P&O MPPT Strategy for Photovoltaic Systems: Simulation and Performance Evaluation
Mr. Pratyush Verma, Lead Engineer, UNO
Minda EV Systems

Industry



A Practical, Optimized Implementation of a Modular SiC Based Fully Digital 5 kW, 3-level Boost MPPT Converter Achieving 90 W / inch 3 Power Density and 99.2% Peak Efficiency
Mr. Akshat Jain, Sr. Staff Engineer - Technical
Member, STMicroelectronics

Academia



Implementation of a Single DC Source Three-Phase Voltage-Boosting Inverter Using Switched-Capacitor Topology
Prof. (Dr.) Pallavee Bhatnagar, Professor,
National Institute of Technical Teachers
Training & Research, Bhopal

Packaging & System Reliability



Chair:
Mr. Vishalpari Gauswami, Development,
Fuji Electric

Industry



Board Level Reliability of Micro-Leaded Automotive MOSFETs of Varying Package Dimensions
Mr. Aniket Kulkarni, Sr. Power Applications
Engineer, Nexperia, UK

Industry



A Reliable Method for Optimum MOSFET Selection for Any Given Application
Mr. Aniket Kulkarni, Sr. Power Applications
Engineer, Nexperia, UK

Industry



Modern Package Technologies on Modern PCB Substrates Enable Cost Saving
Mr. Marc Schikowski, Development Engineer,
Nexperia Germany GmbH

Academia



Real-Time EV Powertrain Fault Diagnostics Using IoT and FMEA-Guided Machine Learning Strategy
Mr. Shreyas Thombare, Student, Master
of Science Program, Indian Institute of
Technology Kharagpur

Industry



Utilizing Frequency Response Analysis in Diverse Power Converter Designs
Mr. Niranjn Hegde, Software Design
Engineer, Tektronix India Pvt. Ltd.

Academia



Design and Reliability Considerations for Top-Side Cooled Power Semiconductors in Automotive Power Electronics
Mr. Sacha Cazzitti, Ph.D. Scholar
The University of Manchester, UK

High Frequency Power Electronic Convertors and Inverters



Chair:
Prof. (Dr.) Shiladri Chakraborty, Assistant
Professor, Department of Electrical
Engineering, IIT Bombay

Government



GaNFET-Based High-Frequency Power Amplifier with FPGA-Driven PWM and Audio Codec Frontend for Piezoelectric Load
Mr. Rahul P. Krishnan, Scientist-C, Naval
Physical & Oceanographic Laboratory, Defence
Research and Development Organization
(DRDO) Ministry of Defence, Govt. of India

Industry



DC Voltage Control Strategies for Vienna Rectifiers in No-Load and Light-Load Conditions
Mr. Vulavakayala Siva, Senior Engineer,
TMEIC Industrial Systems India Private Limited

Industry



Multimode Bidirectional EV Charger with V-to-X Capability
Mr. Datta Rajaram Sagare, Assistant Vice
President - Technology, TVS Motor Company

Industry



QR-ZVS based Secondary Side Controlled Low-Profile Adapter for USB-C PD Applications
Ms. Manvi Gupta, Staff Applications Engineer,
Infineon Technologies Pvt. Limited

Advanced Power Semiconductors



Chair:
Mr. Saumitra Singh, Director, Infineon
Technologies

Industry



Gate Driving Techniques for Common Source GaN Bi-Directional Switch (BDS)
Mr. Shubhendu Sitoke, Staff Engineer
- Applications, Infineon Technologies,
Semiconductor India Pvt. Ltd.



Industry

Influence of Cooling Fin on Tj of HVIGBT Power Module

Mr. Yu Ando, Marketing And Application Engineering Sect.b Product Strategy Dept., Mitsubishi Electric Corporation Power Device Works



Industry

An Enhanced Parametric Testing Model for Optimizing Test Time and Cost in Semiconductor Chips

Mr. Jagdish Sahu, Silicon Validation Engineer, L&T Semiconductor Technologies



Industry

Comparative Analysis of 100 V TOLL MOSFET Power Losses of Motor Drive Inverter Application and its Impact on Efficiency

Ms. Mona Patil, Asst. Manager, Silicon Power Device Business Unit, ROHM Semiconductor India

Digital Power Conversion



Chair:

Mr. Ranajay Mallik, Senior Principal
- Power Conversion & EV Charging Infrastructure, STMicroelectronics



Industry

SOC Solution for Light Electric Vehicles with 240 W USB-C Charging and 3P BLDC Motor Control

Mr. Ankit Tripathi, Sr. Staff Engineer, Infineon Technologies



Industry

EZ-PD™ PMG1-B2 SOC Solution for USB Power Delivery (PD) Integrated 240 W Battery Charging

Mr. Blesson Easo Varghese, Sr. Power Applications Engineer, Infineon Technologies

Power Quality Solutions



Chair:

Mr. Ranajay Mallik, Senior Principal
- Power Conversion & EV Charging Infrastructure, STMicroelectronics



Government

Mitigation of Power Quality Issues in a Dairy Plant using a STATCOM with Remote Monitoring and Control

Ms. Manju A. S., Scientist E, Centre for Development of Advanced Computing (CDAC, Ministry of Electronics & IT)

Motor Drive & Intelligent Motion Control



Chair:

Mr. Sreekanth Menoth, Head, Product Development Program, TMEIC India

Academia

Finite Element Analysis of Axial Flux Limited Angle Torque Machine used for Fuel Injection in Internal Combustion Engines

Mr. Talluri Abhishek, Research Scholar, Indian Institute of Technology, Dharwad



Passive Components and Integration



Chair:

Mr. Ranajay Mallik, Senior Principal
- Power Conversion & EV Charging Infrastructure, STMicroelectronics



Industry

Safe Failure Modeling and Lifetime Prediction of DC Film Capacitors Using Standards-Compliant Reliability Analysis

Mr. Gopal Pawar, Manager - Research & Development Department, Viz Technologies



Exhibitors & Partners

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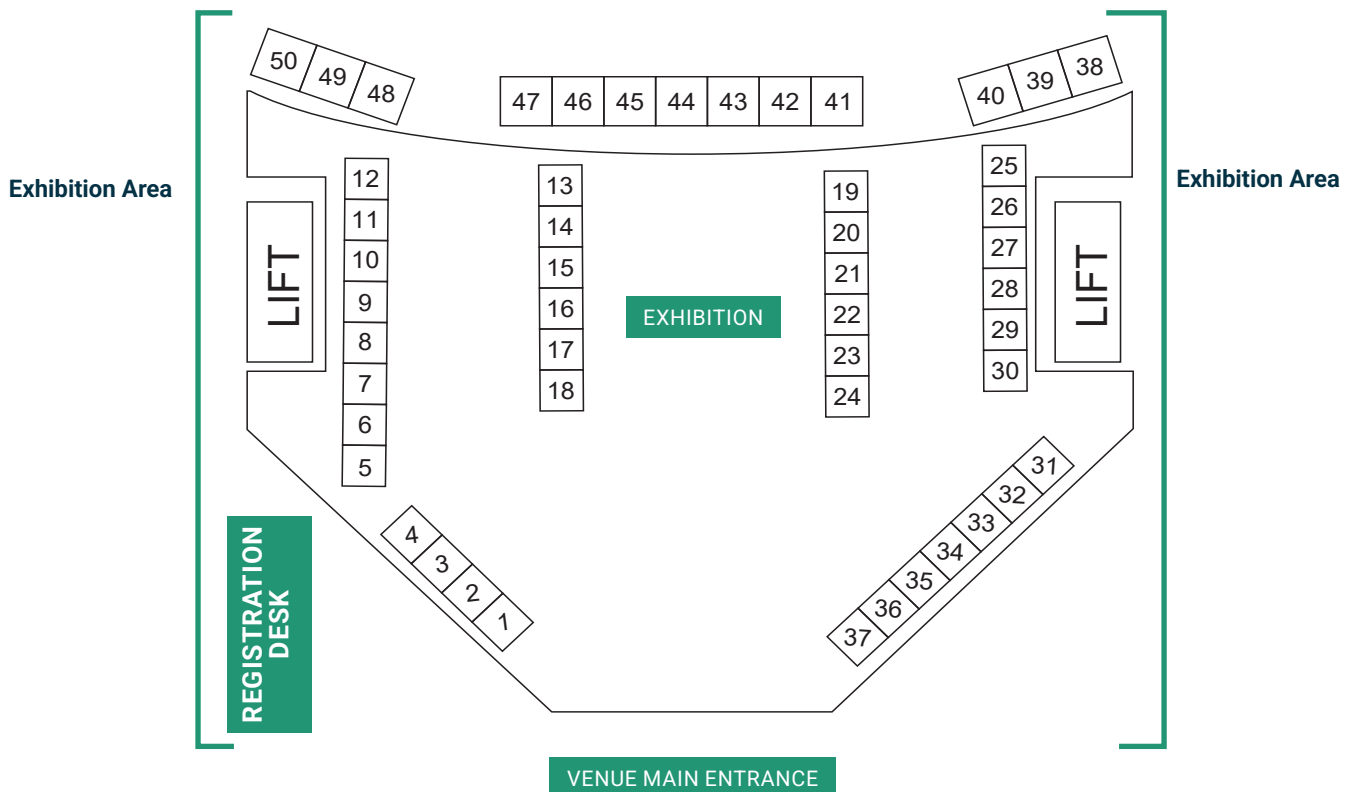
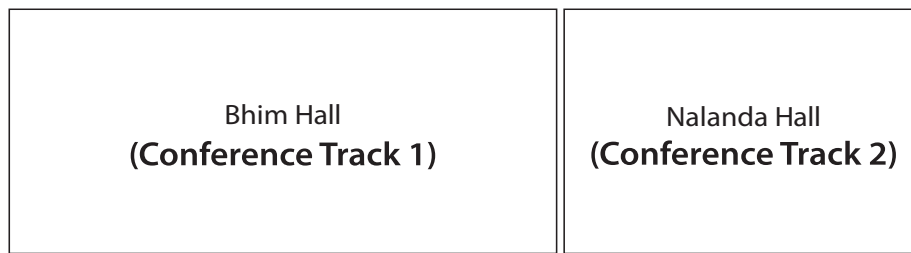


Media Partners



Venue Layout

Dr. Ambedkar International Centre, New Delhi, India Ground Floor



Exhibitor List

Stalls 1,2,3,4	- Infineon Technologies	Stall 30	- Electronicon GmbH
Stalls 5,6,7	- Rohm Semiconductors India	Stalls 31,32,33,34	- MinebeaMitsumi Inc.
Stall 8	- MacDermid Alpha Electronics Solutions	Stalls 35,36	- Semikron Danfoss
Stall 9	- Changzhou Keruier Technology Co., Ltd	Stall 37	- Proterial India
Stall 10	- Pink GmbH Thermosysteme	Stall 38, 39, 40	- Pantronics India
Stall 11	- SCS Power	Stall 41, 42	- Fuji Electric India
Stall 12	- Vincotech GmbH	Stall 43	- Viz Technologies
Stalls 13,14	- AIC Tech Japan	Stall 44	- Indian Institute of Science Bangalore
Stalls 15,16,17,18	- Mitsubishi Electric	Stall 45	- IIT Bombay
Stall 19	- Aperam Alloys India	Stall 46	- IIT Delhi
Stall 20	- Transfer Multisort Elektronik	Stall 47	- Centre for Development of Advanced Computing (CDAC)
Stall 21	- Caltest Instruments GmbH (Pacific Power Source)		Ministry of Electronics and Information Technology, Govt. of India
Stall 22	- Clas-Sic Wafer Fab Ltd	Stall 48	- QBlocks
Stall 23	- Shiima Electronics Inc.	Stall 49	- Pfarr Stanztechnik GmbH
Stall 24	- Desai Electronics	Stall 50	- dSPACE GmbH
Stalls 25, 26	- Arrow Electronics		
Stalls 27, 28	- Littelfuse		
Stall 29	- SiCSem Private Limited		

Background information on Messe Frankfurt Trade Fairs India Pvt Ltd.

Since 1991, the Messe Frankfurt team in India devoted its steadily growing activities to a single goal: bringing people and markets together – initially starting out as a Representative Office in India to a 100% subsidiary of Messe Frankfurt GmbH in 1998.

Messe Frankfurt India has been active in the domestic market for over the last two decades. A thorough understanding of market dynamics, high degree of professionalism and dedication to our clients is what has made Messe Frankfurt India successful. Besides organising impressive trade fairs within the country, the company is also responsible for promoting Indian brands in countries across the globe through sales and support of Messe Frankfurt's outbound trade fairs for a host of verticals. As a result, India has emerged as the 2nd largest exhibiting nation among 139 countries at Frankfurt Fairs – with more than 2,500 Indian manufacturers and scores of visitors, being present every year.

With offices in Mumbai and Delhi, a team of over 100 staff serve the B2B markets of the Indian sub-continent, Messe Frankfurt India has enjoyed remarkable growth over the last two decades, and has come to be known as the company that creates platforms for new markets in India. (<https://www.in.messefrankfurt.com>)

Organizers of: **E-MOBILITY** INDIA FORUM **RailIndia** CONFERENCE & EXPO

Background information on Mesago Messe Frankfurt GmbH

Mesago, founded in 1982 and located in Stuttgart, specializes in exhibitions and conferences on various topics of technology. The company belongs to the Messe Frankfurt Group. Mesago operates internationally and is not tied to a specific venue. With around 160 members of staff Mesago organizes events for the benefit of more than 3,300 exhibitors and over 110,000 trade visitors, conference delegates and speakers from all over the world. Numerous trade associations, publishing houses, scientific institutes and universities work with Mesago closely as advisers, co-organizers and partners. (<https://corporate.mesago.com/>)



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ASIA NEW DELHI conference

9 - 10 December, 2025

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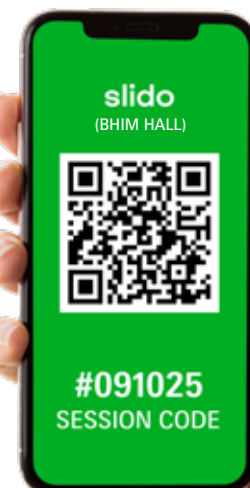
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